

FIG. 1 is a block diagram of a network system 10. The system includes a central server 14, a network 16, and four terminals 12. The central server 14 is connected to the network 16. Each terminal 12 is also connected to the network 16. The network 16 is represented by a cloud shape. The terminals 12 are represented by rectangular boxes. The central server 14 is represented by a rectangular box. The network system 10 is indicated by a curved arrow pointing to the entire diagram.

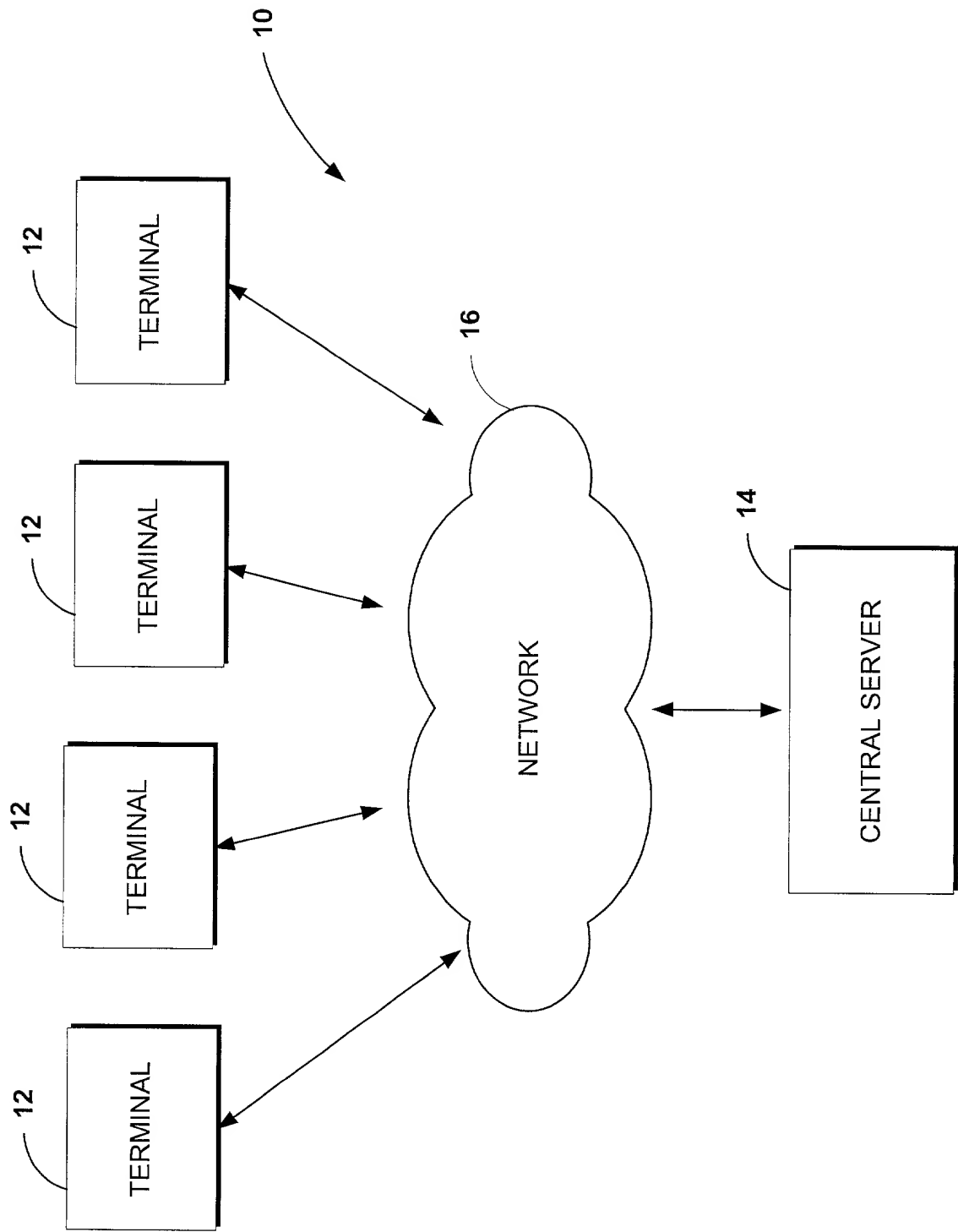


FIG. 1.

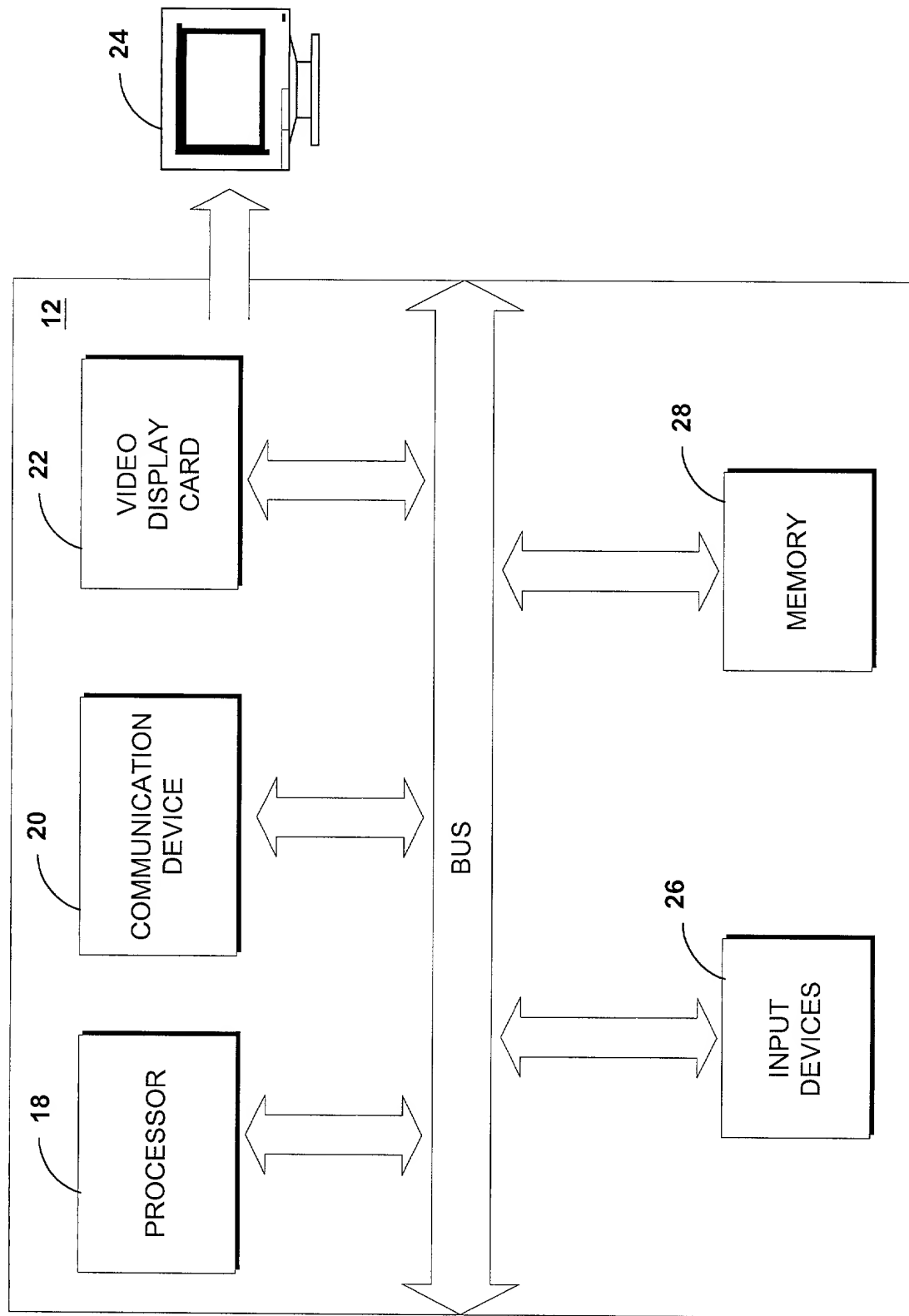


FIG. 2.

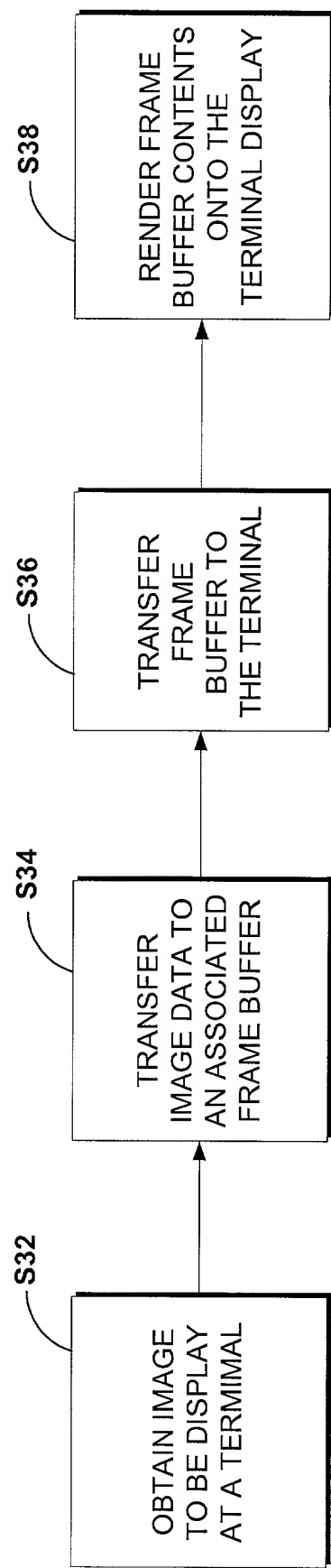


FIG. 3.

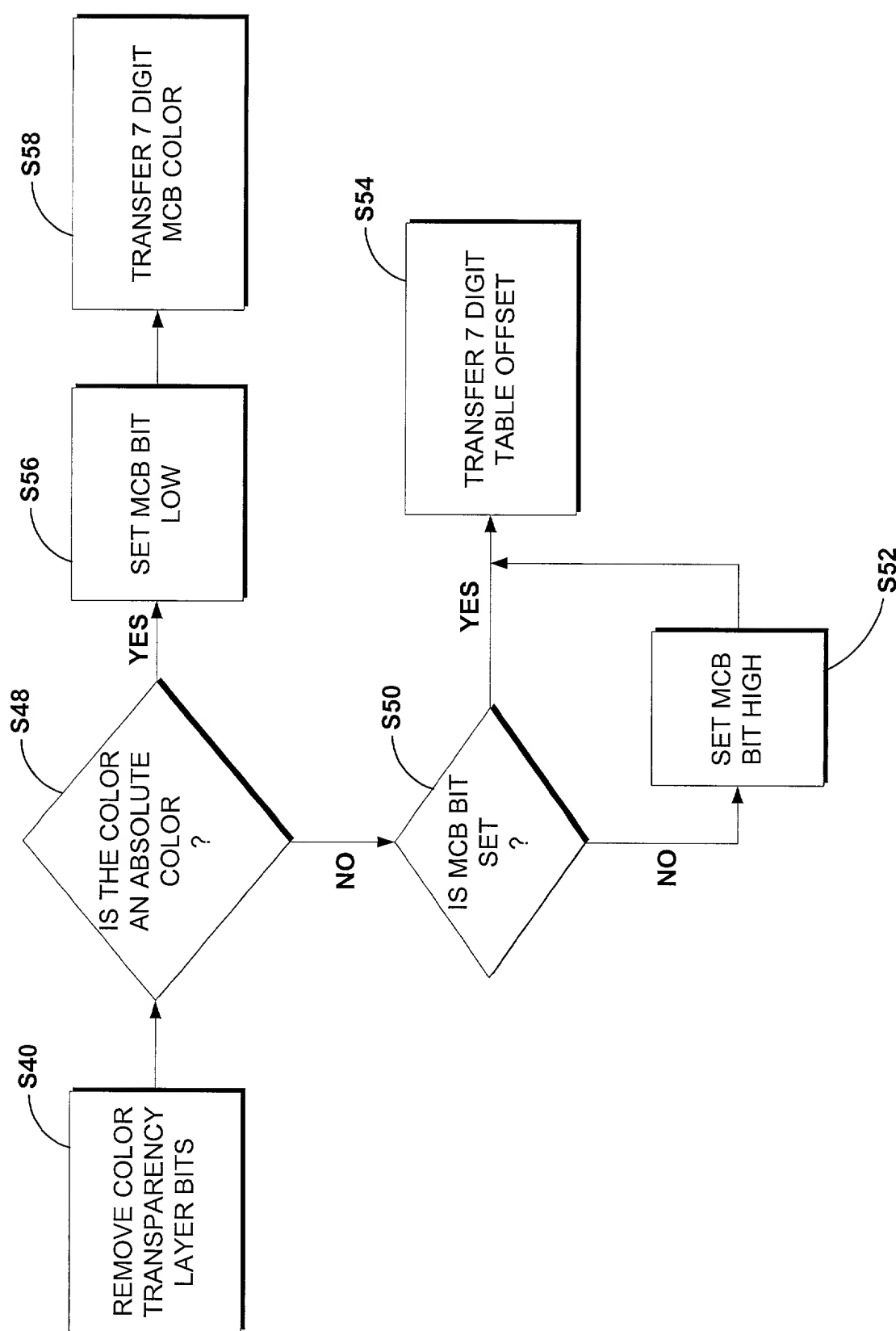


FIG. 4.

FIG. 5 is a block diagram of a system 400 for processing data. The system 400 includes a processor 402, a memory 404, and a network interface 406. The processor 402 is connected to the memory 404 and the network interface 406. The memory 404 stores data and instructions. The network interface 406 is connected to a network 408. The system 400 is configured to receive data from the network 408, process the data, and store the processed data in the memory 404. The processor 402 may be a microprocessor, a microcontroller, or a digital signal processor. The memory 404 may be a random access memory (RAM), a read only memory (ROM), or a non-volatile memory. The network interface 406 may be a wired or wireless interface. The network 408 may be a local area network (LAN), a wide area network (WAN), or the Internet.

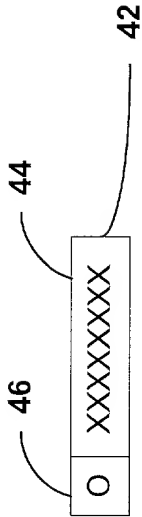


FIG. 5.

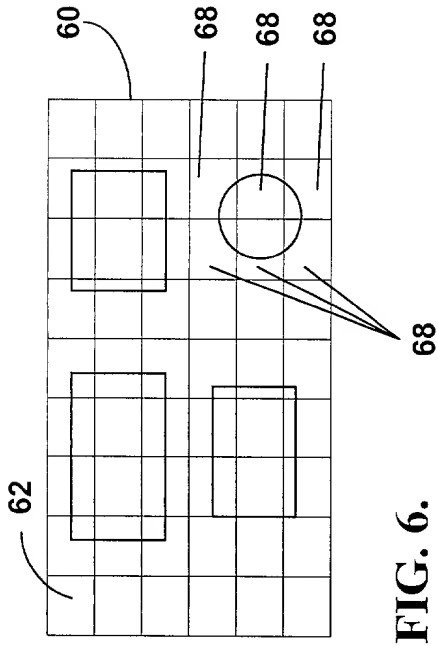


FIG. 6.

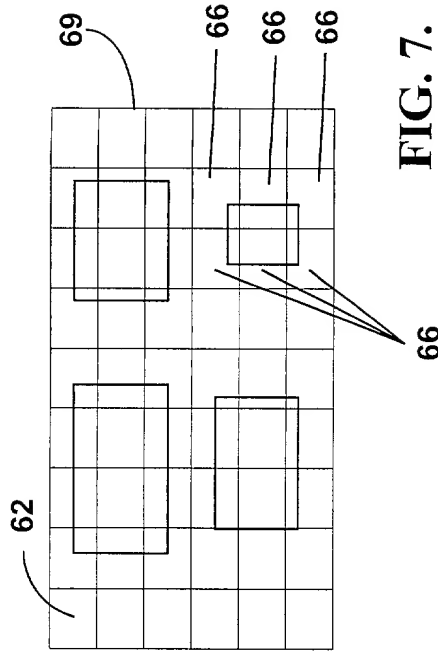


FIG. 7.

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AB	CD	EF	GH	IJ	KL	MN	OP	QR
ST	UV	WX	YZ	12	34	56	78	90
00	01	02	03	04	05	06	07	08
09	10	11	12	13	14	15	16	17
18	19	20	21	22	23	24	25	26
27	28	29	30	31	32	33	34	35

72

FIG. 8.

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AB	CD	EF	GH	IJ	KL	MN	OP	QQ
ST	UV	WX	YZ	11	34	56	78	90
00	01	02	03	04	05	06	07	08
09	10	11	12	13	14	15	16	18
18	19	20	21	22	23	24	25	26
27	28	33	30	31	32	33	34	35

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FIG. 9.

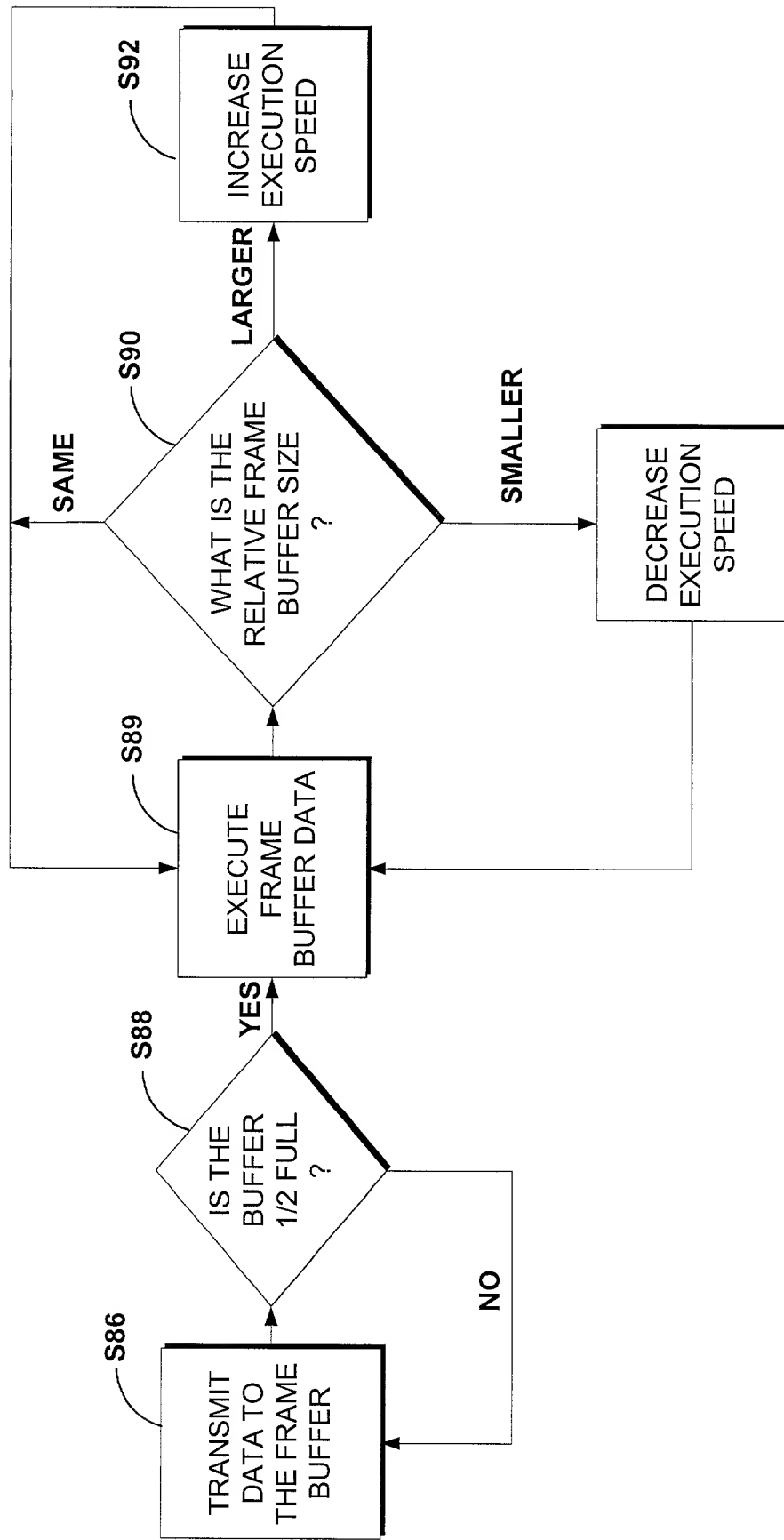


FIG. 10.

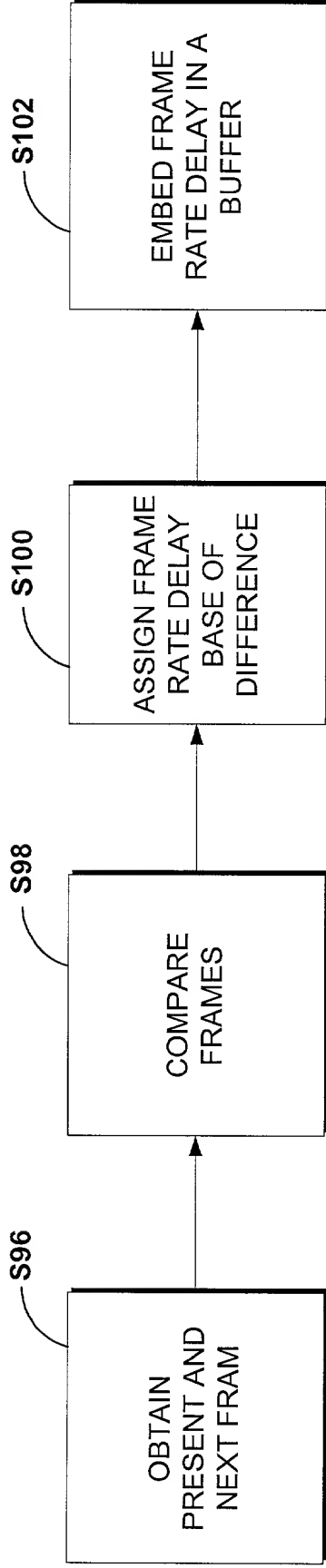


FIG. 11.

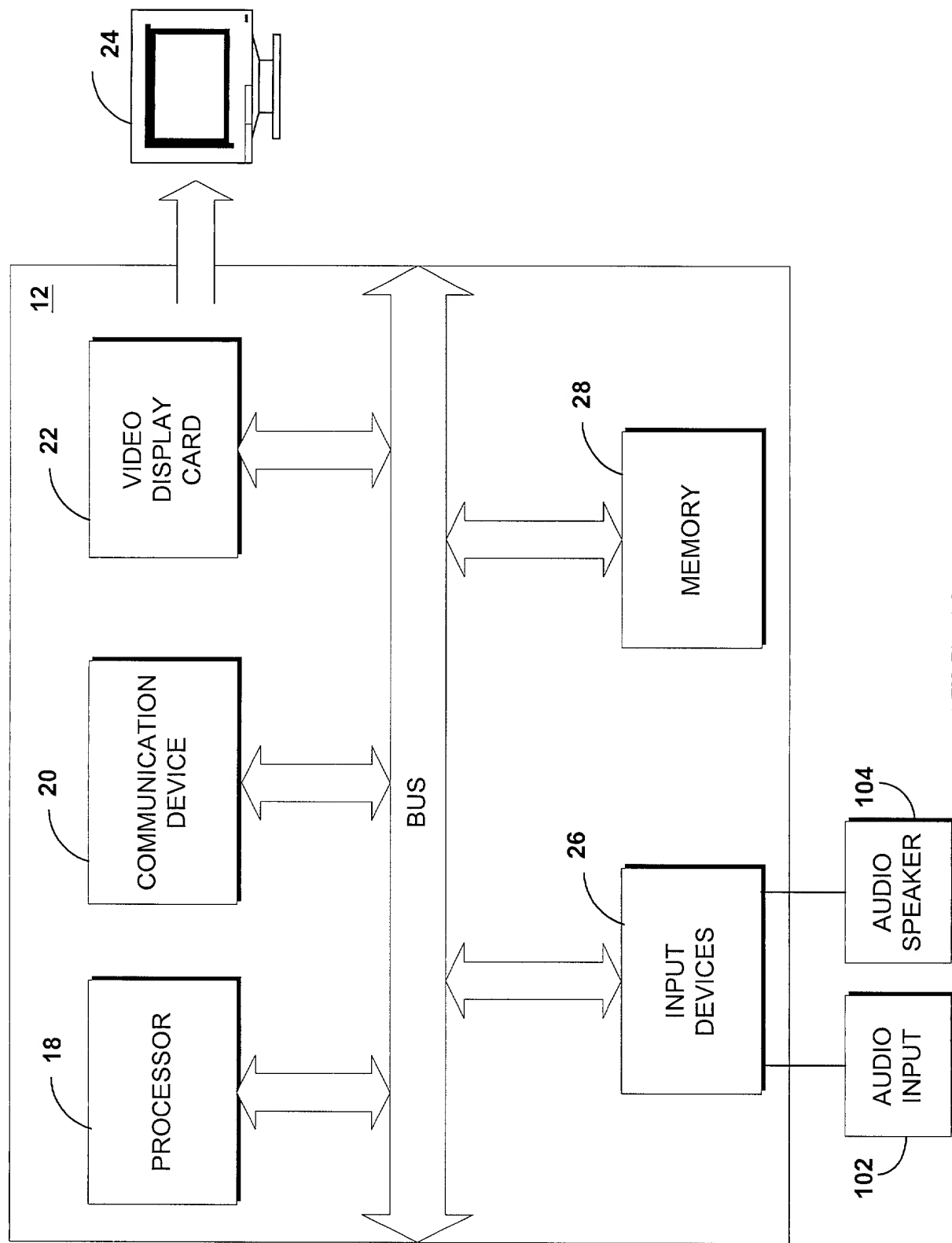


FIG. 12.